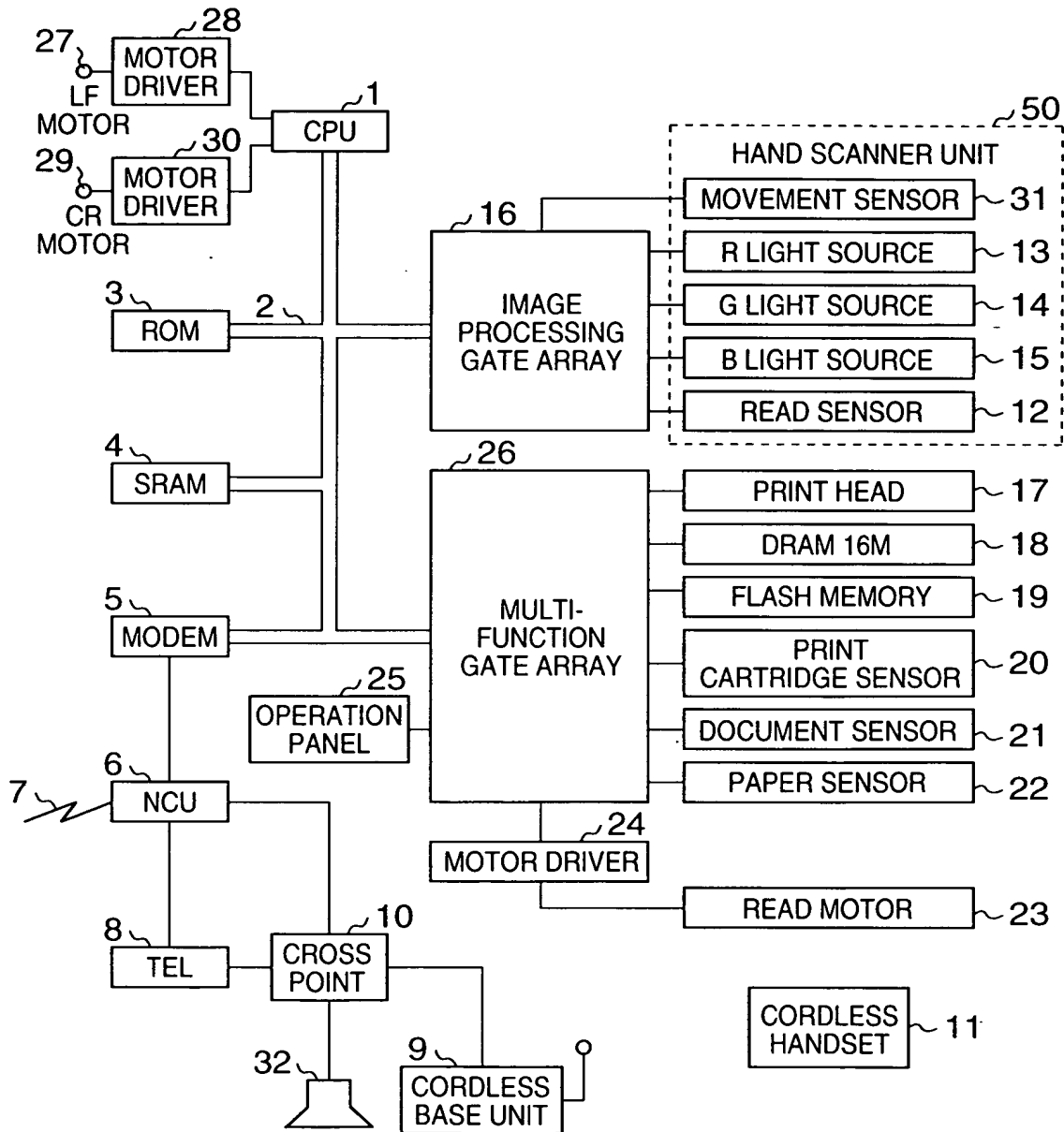


FIG. 1



# FIG. 2

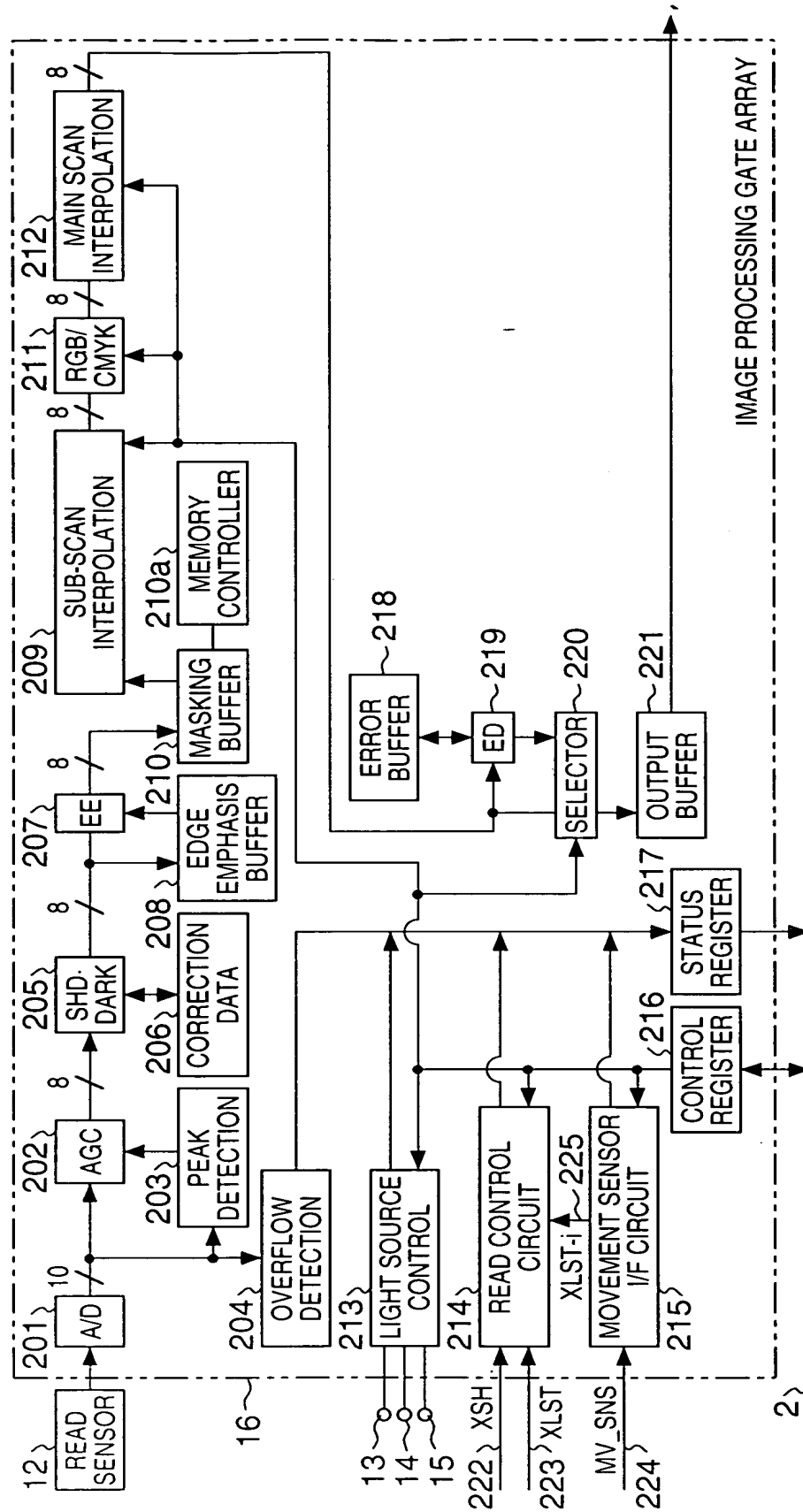


FIG. 3

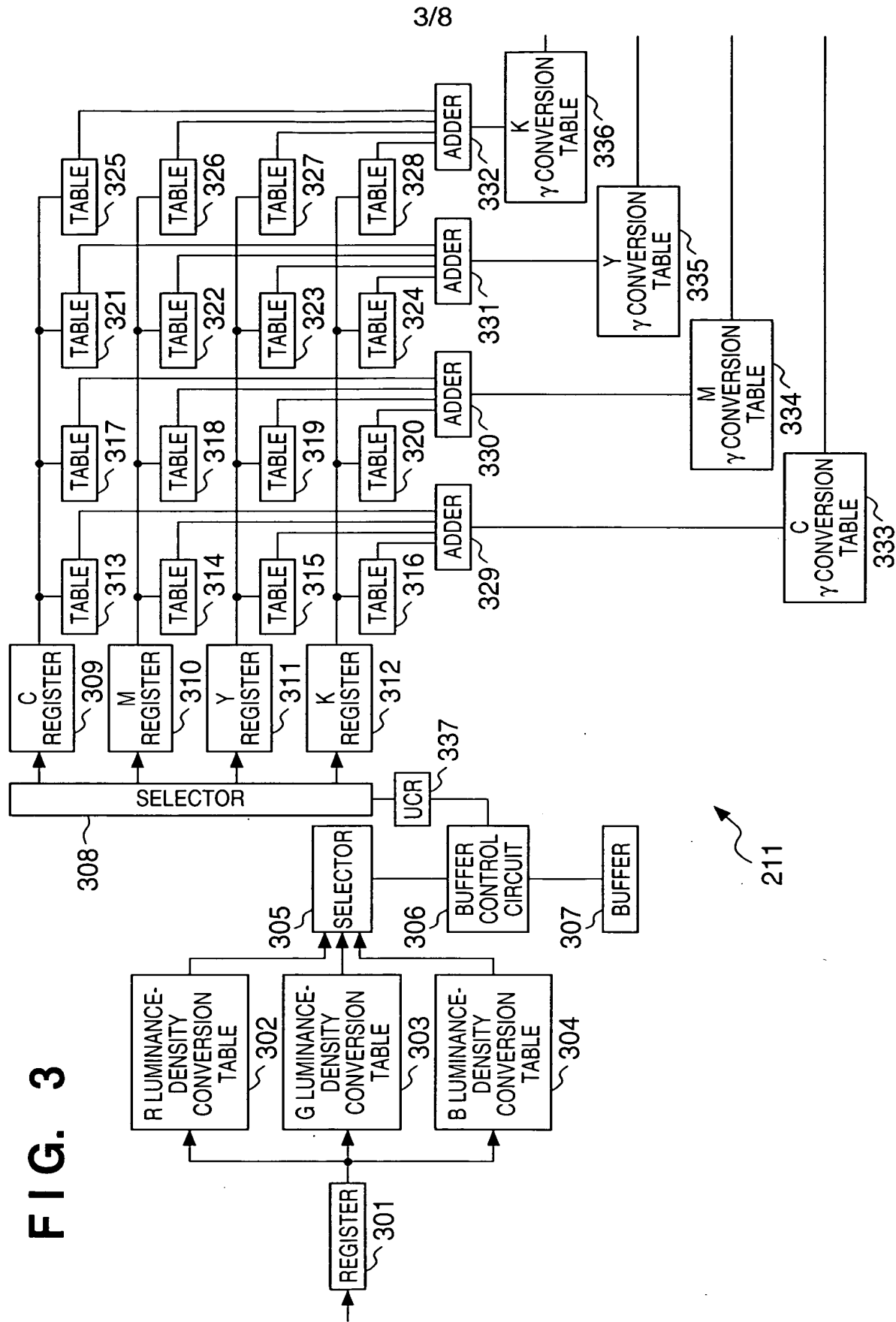


FIG. 4A

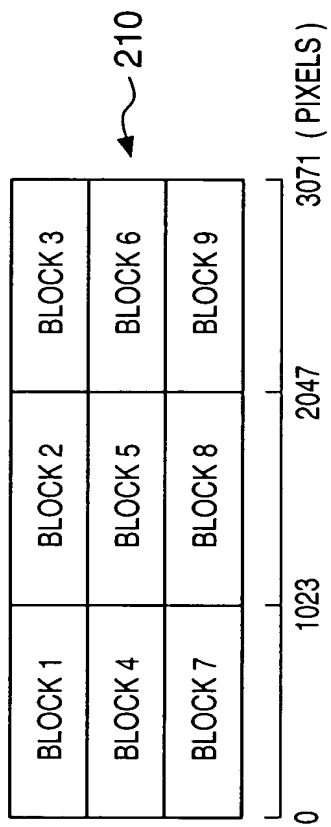


FIG. 4B

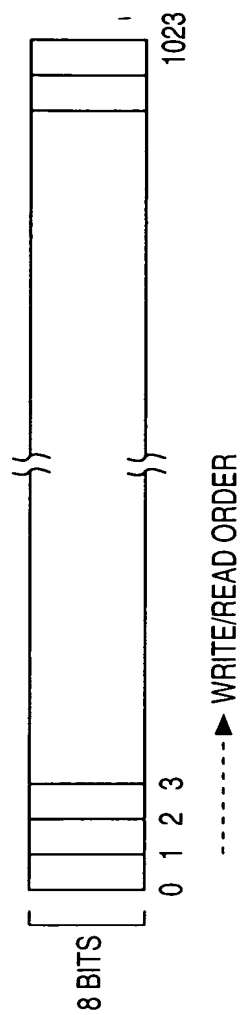


FIG. 5

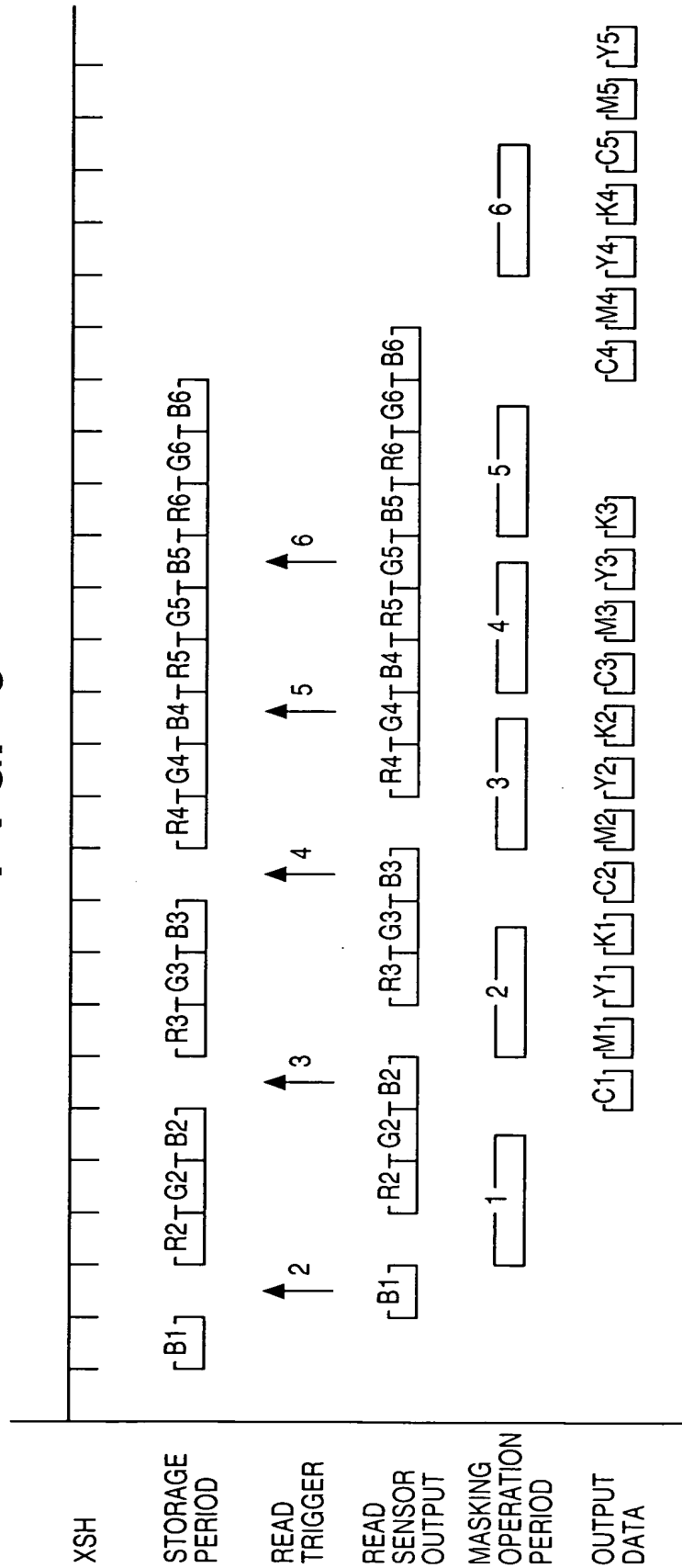
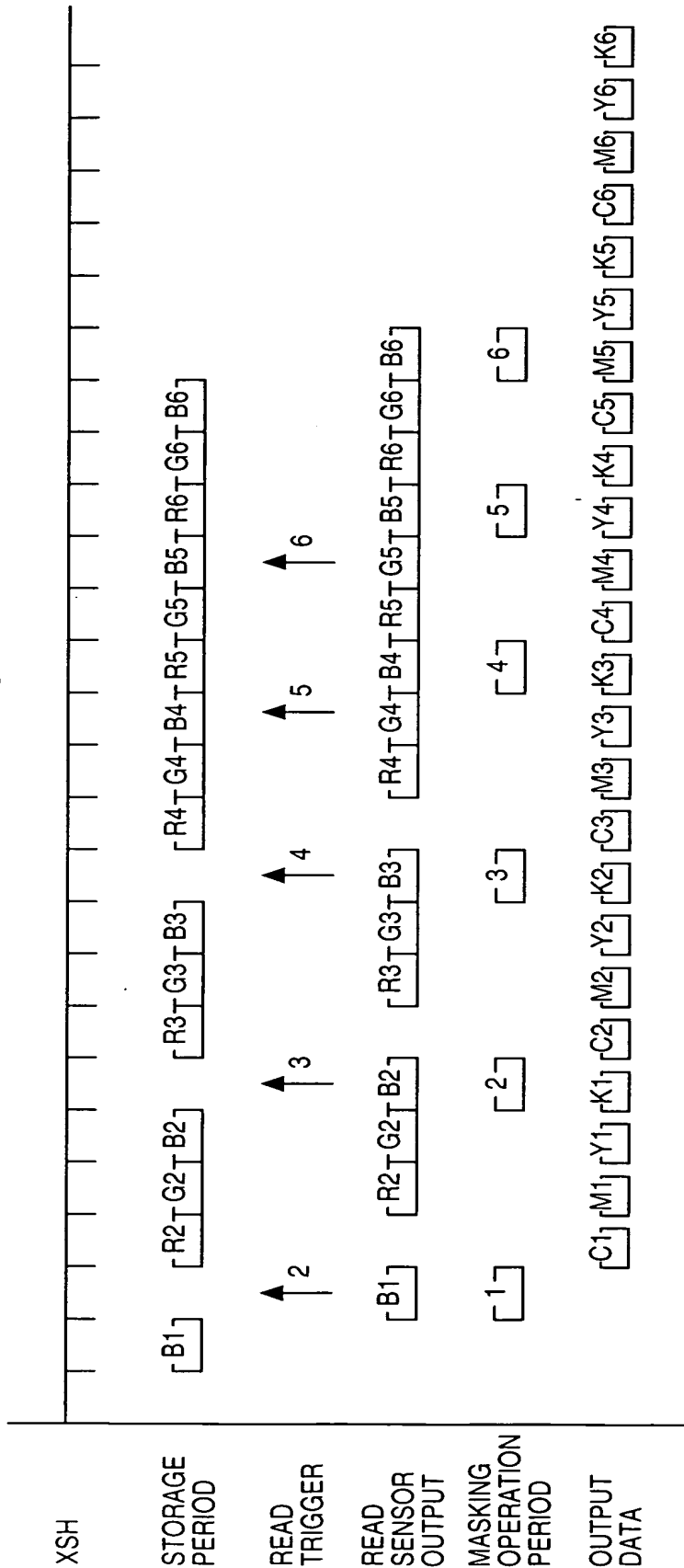


FIG. 6



The diagram illustrates the timing of the 74VHC163 4-bit counter. The XSH signal is a periodic square wave. The MV\_SNS signal is a square wave that is high during the storage period. The COUNTER VALUE is a 4-bit counter that increments during the storage period. The XLST\_i signal is a square wave that is high during the storage period. The STORAGE PERIOD is a square wave that is high during the storage period. The ANALOG SIGNAL INPUT PERIOD is a square wave that is high during the storage period. The SUB-SCAN INTERPOLATION OUTPUT is a square wave that is high during the storage period.

# FIG. 8

